

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.:

10/632,186

Filing Date:

July 30, 2003

Confirmation No.:

3961

First Named Inventor:

Yi Ding

Assignee:

ProMOS Technologies, Inc.

Examiner:

Nguyen, Thanh T.

Art Unit:

2813

Attorney Docket No.:

M-15241 US

San Jose, California October 12, 2004

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR § 1.97(C)

## Dear Sir:

Pursuant to 37 CFR § 1.56, § 1.97 and § 1.98, the documents listed on the accompanying form PTO-1449 are called to the attention of the Examiner for the above patent application. Copies of these documents are enclosed except for United States Patent and United States Published Patent Applications.

Citation of these documents shall not be construed as:

- 1. an admission that the documents are necessarily prior art with respect to the instant invention;
- 2. a representation that a search has been made, other than as described above; or
- 3. an admission that the information cited herein is, or is considered to be material to patentability as defined in § 1.56(b).

Please charge the fee of \$180.00 as set forth in 37 CFR 1.17(p) to deposit account 50-2257. Please charge any amount underpaid, or credit the amounts overpaid, to the same deposit account. This paper is being submitted in duplicate.

TYDDEGG	BEATT	TADET	NIC
<b>EXPRESS</b>	MAIL	LABEL	/ NU.:

EV 513 137 893 US

Respectfully submitted,

Michael Shenker Patent Attorney

Reg. No. 34,250

Telephone: (408) 392-9250, Ext. 212

Michael Showles

Law Offices Of

MacPherson Kwok Chen & Heid LLP 1762 Technology Drive, Suite 226

San Jose, CA 95110

J.S. Department of Commerce, Patent and Trademark Office					Atty Docket No	).	Serial No.		
					M-15241 US 10/632,186				
INFORMATION DISCLOSURE STATEMENT BY APPLICANT					Applicant(s)				
(Use several sheets if necessary)					Yi Ding				
	(3)				Filing Date Group				
OCT 12	2004				July 30, 2003	2813			
FIRE	AL PARTY		U.S. Pa	tent Documents	· <del>-</del>				
Examination Average Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate		
	AA	6,265,739	Jul. 2001	Yaegashi et al.					
	AB	6,747,310	Jun. 2004	Fan et al.					
	AC	2003/0205776	Nov. 2003	Yaegashi et al.					
	AD	6,468,865	Oct. 2002	Yang et al.					
	AE	6,218,689	Apr. 2001	Chang et al.					
	AF	6,214,669	Apr. 2001	Hisamune					
	AG	6,162,682	Dec. 2000	Kleine					
	AH	6,232,185	May-01	Wang					
	AI	5,912,843	Jun. 1999	Jeng					
	AJ	6,436,764	Aug. 2002	Hsieh					
	AK	6,344,993	5 Feb. 2002	Harari et al.					
	AF	5,668,757	Sept. 1997	Jeng					
	AM	6,355,524	Mar. 2002	Tuan et al.					
		OTHER A	RT (Including Au	thor, Title, Date, Per	rtinent Pages, Etc	c.)			
<del></del>									
					<u> </u>				
			Date Considered						

II C Deports	nent of C	ommerce Dotont on	d Tandomanla Occ			•	Sheet 1 o		
U.S. Department of Commerce, Patent and Trademark Office				Atty Docket 1	Serial No.				
Direction black continue and the second					M-15241 US Unassigned				
INFORMATION DISCLOSURE STATEMENT BY APPLICANT					Applicant(s)				
PU	10	(Use several sheet	s if necessary)		Yi Ding				
U		<u> </u>			Filing Date		Group		
		OCT 12 2004			Filed Herewit	h	Unassigned		
		A. E	U.S.	Patent Documents					
*Examiner Initial		Number	Date	Name	Class	Subclass	Filing Date If Appropriate		
	AA	5,402,371	28 Mar. 1995	Ono					
	AB	5,856,943	5 Jan. 1999	Jenq					
	AC	6,057,575	2 May 2000	Jenq					
	AD	6,130,129	10 Oct. 2000	Chen					
	AE	6,134,144	17 Oct. 2000	Lin et al.					
	AF	6,171,909	9 Jan. 2001	Ding et al.					
	AG	6,200,856	13 Mar. 2001	Chen		· · · · · · · · · · · · · · · · · · ·			
	AH	6,261,903	17 Jul. 2001	Chang et al.					
	AI	6,326,661	4 Dec. 2001	Dormans et al.					
	AJ	6,355,524	12 Mar. 2002	Tuan et al.					
	AK	6,365,457	2 Apr. 2002	Choi					
		OTHER A	RT (Including A	uthor, Title, Date, Per	tinent Pages, Etc	c.)	<u> </u>		
	AL	Shirota, Riichiro " February 2000, No	emories and NA rey, California,	ND Flash Futi pages 22-31.	ire Trend,"				
	AM	Naruke, K.; Yamada, S.; Obi, E.; Taguchi, S.; and Wada, M. "A New Flash-Erase EEPROM Cell with A Sidewall Select-Gate On Its Source Side," 1989 IEEE, pages 604-606.							
	AN	Wu, A.T.; Chan T. Structure With Son	Y.; Ko, P.K.; and arce-Side Injection	d Hu, C. "A Novel Hig on," 1986 IEEE, 584-5	h-Speed, 5-Vol 87.	t Programming	EPROM		
	AO	Mizutani, Yoshihisa; and Makita, Koji "A New EPROM Cell With A Sidewall Floating Gate Fro High-Density and High Performance Device," 1985 IEEE, 635-638.  Ma, Y.; Pang, C.S.; Pathak, J.; Tsao, S.C.; Chang, C.F.; Yamauchi, Y.; Yoshimi, M. "A Novel High Density Contactless Flash Memory Array Using Split-Gate Source-Side-Injection Cell for 5V-Only Applications," 1994 Symposium on VLSI Technology Digest of Technical Papers, pages 49-50.  Mih, Rebecca et al. "0.18um Modular Triple Self-Aligned Embedded Split-Gate Flash Memory," 2000 Symposium on VLSI Technology Digest of Technical Papers, pages 120-121.							
	AP								
	AQ								
·	AR	Ma, Yale et al., "A Dual-Bit Split-Gate EEPROM (DSG) Cell in Contactless Array for Single Vcc High Density Flash Memories," 1994 IEEE, 3.5.1-3.5.4.							
xaminer			Date Considered			······································			
EXAMINER: itation if not in	Initial if conform	reference considere ance and not consid	d, whether or not ered. Include co	citation is in conformately of this form with yo	ance with MPEF our communicati	P 609; Draw lir ion to applican	ne through t.		

U.S. Departm	nent of	ommerce Patent and	d Trademark Offi	ice	ΙΔ+	ty Docket N	No.	Sheet 2		
U.S. Department of commerce, Patent and Trademark Office				Atty Docket No. M-15241 US			Serial No.			
INFORMATION DISCLOSURE STATEMENT BY APPLICANT					<del> </del>	<del></del>	Unassigned			
(Use several sheets if necessary)					Applicant(s)					
6		(Ose several sheet			Yi Ding					
					<del> </del>	ing Date		Group		
					File	ed Herewit	h	Unassigned		
*Examiner		Degument	U.S.	Patent Documents		<del></del>	·			
Initial		Document Number	Date	Name		Class	Subclass	Filing Date If Appropriate		
	AS	6,437,360	20 Aug. 2002	Cho et al.				птрргоргии		
	AT	6,438,036	20 Aug. 2002	Seki et al.	, -·- <u>.</u>			1		
	AU	6,486,023	26 Nov. 2002	Nagata						
	AV	6,541,324	1 Apr. 2003	Wang						
	AW	2002/0064071 A1	30 May 2002	Takahashi et al.						
	AX	2002/0197888 A1	26 Dec. 2002	Huang et al.						
	AY	6,266,278	24 Jul. 2001	Harari et al.						
t and there is the same of	AZ	5,901,084	.4 May 1999	Ohnakado						
	BA	6,518,618	11 Feb. 2003	Fazio et al.			<del> </del>			
	BB	6,541,829	1 Apr. 2003	Nishinohara et al.						
	BC	6,414,872	2 Jul. 2002	Bergemont et al.						
		OTHER A	RT (Including A	uthor, Title, Date, Per	tinent	Pages, Etc	2.)			
	BD	Spinelli, Alessand	ro S., "Quantum- ational Conferen	Mechanical 2D Simuloce on Simulation of Se	ulation of Surface-and Buried-Channel p- Semiconductor Processes and Devices: SISPAD					
	BE	Kim, K.S. et al. "A and 1 Gbit Flash M	Novel Dual Stri	ng NOR (DuSnor) Me IEEE 11.1.1-11.1.4	mory	Cell Tech	nolgy Scalabe	to the 256 Mbit		
	BF	BF Bergemont, A. et al. "NOR Virtual Ground (NVG)- A New Scaling Cond EEPROM and its Implementation in a 0.5 um Process," 1993 IEEE 2.2.1						gh Density FLAS		
	BG	Van Duuren, Michiel et al., "Compact poly-CMP Embedded Flash Memory Cells For One or Tw Storage," Philips Research Leuven, Kapeldreef 75, B3001 Leuven, Belgium, pages 73-74.								
	ВН									
	BI									
	ВЈ									
caminer			Date Considered							
EXAMINER: ation if not in	Initial if a	reference considered ance and not considered	l, whether or not ered. Include cop	citation is in conformately of this form with yo	ance our co	with MPEP ommunicati	609; Draw lir on to applican	ne through t.		

t , ir

			Sheet 1 of			
U.S. Department	mmerce, Patent and Trademark Office	Atty Docket No.	Serial No.			
<b>O</b>		M-15241 US	10/632,186			
INFORMATIO	N DISCLOSURE STATEMENT BY APPLICANT	Applicant(s)				
	(Use several sheets if necessary)	Yi Ding				
OF		Filing Date Group				
		July 30, 2003	Unassigned			
OCT 12 2004 B	OTHER ART (Including Author, Title, Date, P	ertinent Pages, Etc.)	<u> </u>			
PARADEMENTS A	United States Patent Application No. 10/440,466, entity Nonvolatile Memories From Layers With Protruding P. No.: M-12979 US.	ortions," Filed on May 16, 20	03; Attorney Docket			
AB	United States Patent Application No. 10/440,005, entity To Insulate The Gate From Another Element Of An Interest Docket No.: M-15203 US.	tegrated Circuit," Filed on Mag	y 16, 2003; Attorney			
AC	United States Patent Application No. 10/440,508, entitle Memories Having Select, Floating And Control Gates, 15204 US.					
AD	United States Patent Application No. 10/440,500, entitle Electrical Contact To Conductive Features Having Self Docket No.: M-15205 US.	ed "Integrated Circuits With C -Aligned Edges," Filed on Ma	Openings that Allow y 16, 2003; Attorney			
AE	United States Patent Application No. 10/393,212, entitl Fabrication," Filed on March 19, 2003; Attorney Docke	ed "Nonvolatile Memories Anet No.: M-12902 US.	d Methods Of			
AF	AF United States Patent Application No. 10/411,813, entitled "Nonvolatile Memories With A FI Having An Upward Protrusion," Filed on April 10, 2003; Attorney Docket No.: M-12903 U					
AG	United States Patent Application No. 10/393,202, entitle Structures With Protruding Features," Filed on March 1	ed "Fabrication of Integrated (9, 2003; Attorney Docket No.	Circuit Elements In: M-15151 US.			
АН	United States Patent Application No. 10/631,941, entitle Floating Gates Formed After The Select Gate," Filed or US.					
AI	United States Patent Application No. 10/632,155, entitle Channel Transistors," Filed on July 30, 2003; Attorney		With Buried			
AJ	United States Patent Application No. 10/632,007, entitle Each Cell Has Two Conductive Floating Gates," Filed out.	ed "Arrays Of Nonvolatile Me on July 30, 2003; Attorney Doo	mory Cells Wherin ket No.: M-15223			
AK	United States Patent Application No. 10/631,452, entitle Memory Cell Having Multiple Floating Gates," Filed or US.	ed on July 30, 2003; Attorney Docket No.: M-15229  ntitled "Fabrication Of Gate Dielectric In Nonvolatile				
AL	United States Patent Application No. 10/632,154, entitle Memories In Which A Memory Cell Has Multiple Float Docket No.: M-15230 US.					
AM	United States Patent Application No. 10/631,552, entitle Fabrication," Filed on July 30, 2003; Attorney Docket N		Methods Of			
AN						
Examiner	Date Considered					
*EXAMINER: Initial i citation if not in conform	f reference considered, whether or not citation is in conformance and not considered. Include copy of this form with	mance with MPEP 609; Draw your communication to applic	line through ant.			

U.S. Department of Commerce, Patent and Trademark Office				Atty Docket No. Serial No.				al No.			
				M-15241 US 10/632,186							
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Applicant							
	(Use several sheets if necessary)					Yi Ding					
					Filing Date Group						
					July 30, 2003 Unassigned						
			U.S. Pa	tent Documents							
*Examiner Initial		Document Number	Date	Name	Class Su			Filing ubclass If Appro			
	AA	6,420,231	16 Jul. 2002	Harari et al.		Class	out on	433	Пирріо	priate	
	AB	2003/0218908 A1	27 Nov. 2003	Park et al.						•	
	AC	2004/0004863 A1	8 Jan. 2004	Wang							
	AD										
	AE					-					
	AF										
	AG								, <del>, , , , , , , , , , , , , , , , , , </del>		
	AH	and an arrange of the same of		as the same of the							
			Foreign P	atent Documents							
							-		Trans	slation	
		Document	Date	Country		Class	Subcla	ss	Yes	No	
· · · · · · · · · · · · · · · · · · ·	AI	EP 0 938 098 A2	25 Aug. 1999	Europe							
	AJ										
	AK										
	AL			-							
1		OTHER AI	RT (Including Aut	hor, Title, Date, Pe	rtinen	t Pages, Et	c.)				
	AM		nductive Gates in	10/798,475, entitle Nonvolatile Memor No. M-15296 US.						Filed	
	AN	United States Patent Application No. 10/797,972, entitled "Fabrication of Conductive Lines Interconnecting First Conductive Gates in Nonvolatile Memories Having Second Conductive Gates Provided By Conductive Gates Lines, Wherein The Adjacent Conductive Gate Lines For The Adjacent Columns Are Spaced From Each Other, And Non-Volatile Memory Structures," Filed on March 10, 2004; Attorney Docket No. M-15297 US.									
	AO				-			-			
	AP							<del></del>	······		
Examiner			Date Considered								
*EXAMINER: citation if not i	: Initial if	f reference considered nance and not considered	d, whether or not of ered. Include cop	citation is in conform y of this form with	mance your	with MPE	EP 609; Di	raw lii plican	ne through it.		